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impurity distribution near a channel region, particularly near a surface. A method of controlling the threshold voltage by applying a stress to a channel is also known. For example, a method of applying a tensile stress to a channel for holes is known as a positive channel transistor (PCT) or a buried channel transistor (BCT). Meanwhile, a method of applying a compressive stress to a channel for electrons is known as a negative channel transistor (NCT) or a surface channel transistor (SCT). Methods of applying stresses such as those described above to the channel of a semiconductor device are disclosed in, for example, Japanese Patent Laid-Open Nos. H5-122336, H5-145840, and H6-77576, Japanese Patent Laid-Open Nos. 2000-46127 and 2001-100824, and Japanese Patent Laid-Open No. 2003-096038. An increased concentration of an impurity near a channel causes the threshold voltage to shift to a low-voltage side. In order to decrease the concentration of the impurity near the channel, a method of applying a strong electric field 6d1f23a050

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